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Docket: 0756-1551

IN THE UNITED STATES PATEN	I AND I RADEWARK OFFICE	7/T
In re PATENT application of	67/6	liglos
SHUNPEI YAMAZAKI et al) • • • • • • • • • • • • • • • • • • •	الناقص
Serial No. 08/691,434) Group Art Unit: 2822	
Filed: 08/02/1996) Examiner: Wilczewski ក្ល	
For: METHOD OF FABRICATING) SHOOL TO	p D
SEMICONDUCTOR DEVICES AND	(a)	ii Ii
APPARATUS FOR PROCESSING A) 10 K CEN	Y.
SEMICONDUCTOR	NTER NTER	9
	22	

Honorable Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a.

INFORMATION DISCLOSURE STATEMENT

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

Respectfully submitted,

05/31/2002 ADSMAN1 00000016 08631434

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180.00 OP

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D. 20231, on